

MODBUS MAP

(GFR-D12)

2008. 8. 20

(주) 운 영

MODBUS System Control for GFR

Communication Control	RS-485
Baud rate	9600, 19200, 38400 bps
Data Frame	1 Start bit, 8 Data bit, 1 Stop bit (Total 10 bit)
Parity	Non Parity
Slave No. 지정	1 ~ 100 (Device Setting)

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1. SYSTEM STATUS CHECK (Input Register)

Code	Address	Parameter	Bytes	R/W	Unit	Data Mag.	Data Type
System Status Check							
04h	30000	Data HI bit 15 ~ bit 8	2	R	A		unsigned 16
		Reserved					
		Data LOW bit 7 ~ bit 3					
		Reserved					
		bit 2					
		BUZZER (0=OFF, 1=ON)					
		bit 1					
		AUTO RESET (0=OFF, 1=ON)					
		bit 0					
		FREQUENCY SET (0=60Hz, 1=50Hz)					

2. FAULT STATUS CHECK (Input Register)

Code	Address	Parameter	Bytes	R/W	Unit	Data Mag.	Data Type	
Line Phase Check (Fault Monitoring)								
04h	30001	Data HI	2	R	A		unsigned 16	
		bit 7						Reserved
		bit 6						Reserved
		bit 5						Line 12 Fault
		bit 4						Line 11 Fault
		bit 3						Line 10 Fault
		bit 2						Line 9 Fault
		bit 1						Line 8 Fault
		bit 0						Line 7 Fault
		Data LOW						
		bit 7						Reserved
		bit 6						Reserved
		bit 5						Line 6 Fault
		bit 4						Line 5 Fault
		bit 3						Line 4 Fault
		bit 2						Line 3 Fault
bit 1	Line 2 Fault							
bit 0	Line 1 Fault							
		(0=Not Fault, 1=Fault)						

3. LINE CURRENT CHECK (Input Register)

Code	Address	Parameter	Bytes	R/W	Unit	Data Mag.	Data Type				
Analog Data Check (Current Monitoring)											
04h	30002	Line 1 Current	2	R	A	1000	unsigned 16				
		ex)									
		Current						Data HI	Data LO		
	0.20A	00h						14h			
	~										
	10.00A	03h						E8h			
	30003	Line 2 Current									
	30004	Line 3 Current									
	30005	Line 4 Current									
	30006	Line 5 Current									
	30007	Line 6 Current									
	30008	Line 7 Current									
	30009	Line 8 Current									
	30010	Line 9 Current									
30011	Line 10 Current										
30012	Line 11 Current										
30013	Line 12 Current										

4. SET CHECK (Holding Register)

Code	Address	Parameter	Step	Data Mag.	Bytes	R/W	Unit	Data Type
Channel Operating Set (OCGR Set)								
03h	40000	Line 1 Current Setting TAP Range (Lock, 0.1 ~ 10.0 A)	1		2	R/W	A	unsigned 16
		ex) Current Data HI Data LO LOCK 00h 00h 0.20A 00h 02h ~ 2.00A 00h 14h						
	40001	Line 1 Time Delay Setting TAP Range (Inst, 0.1 ~ 10 Sec)						
		ex) Time Data HI Data LO INST 00H 00h 0.10 00h 01h ~ 10.00 03h E8h						
	40002	Line 2 Current Setting						
	40003	Line 2 Time Delay Setting						
	40004	Line 3 Current Setting						
	40005	Line 3 Time Delay Setting						
	40006	Line 4 Current Setting						
	40007	Line 4 Time Delay Setting						
	40008	Line 5 Current Setting						
	40009	Line 5 Time Delay Setting						
	40010	Line 6 Current Setting						
	40011	Line 6 Time Delay Setting						
	40012	Line 7 Current Setting						
	40013	Line 7 Time Delay Setting						
	40014	Line 8 Current Setting						
	40015	Line 8 Time Delay Setting						
	40016	Line 9 Current Setting						
	40017	Line 9 Time Delay Setting						
	40018	Line 10 Current Setting						
	40019	Line 10 Time Delay Setting						
	40020	Line 11 Current Setting						
	40021	Line 11 Time Delay Setting						
40022	Line 12 Current Setting							
40023	Line 12 Time Delay Setting							

5. FAULT MEMORY CHECK (Holding Register)

Code	Address	Parameter	Step	Data Mag.	Bytes	R/W	Unit	Data Type	
Fault Check									
03h	40024	Line 1 Fault Memory Check ex) Current Data HI Data LO 0.10 00h 01h ~ 10.00 03h E8h (0=Not Fault, XXh=Fault)			2	R			unsigned 16
	40025	Line 2 Fault Memory Check							
	40026	Line 3 Fault Memory Check							
	40027	Line 4 Fault Memory Check							
	40028	Line 5 Fault Memory Check							
	40029	Line 6 Fault Memory Check							
	40030	Line 7 Fault Memory Check							
	40031	Line 8 Fault Memory Check							
	40032	Line 9 Fault Memory Check							
	40033	Line 10 Fault Memory Check							
	40034	Line 11 Fault Memory Check							
	40035	Line 12 Fault Memory Check							

6. FAULT CLEAR (Control Output)

Code	Address	Parameter	Bytes	R/W	Unit	Data Type
Fault Memory Clear						
05h	0	Clear Fault Memory clear : 00	2	W	없음	unsigned 16