

MODBUS-MAP

(OCR)

WYR-OC2DC, WYR-OC3DC

Rev.1

(주) 운 영

MODBUS System Control for OCR

Communication Control	RS-485
Baud rate	9600, 19200, 38400 bps
Data Frame	1 Start bit, 8 Data bit, 1 Stop bit (Total 10 bit)
Parity	Non Parity
Slave No. 지정	1 ~ 32 (Device Setting)

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1. SYSTEM STATUS CHECK (Input Register)

Code	Address	Parameter	Bytes	R/W	Unit	Data Mag.	Data Type
System Status Check							
04h	30000	Data HI bit 15 ~ bit 8 Reserved Data LOW bit 7 ~ bit 1 Reserved bit 0 System Error 0=Normal, 1=Error	2	R	A		unsigned 16

2. FAULT STATUS CHECK (Input Register)

Code	Address	Parameter	Bytes	R/W	Unit	Data Mag.	Data Type
Line Phase Check (Fault Monitoring)							
04h	30001	Data HI bit 15 ~ bit 8 Reserved Data LOW bit 7 Reserved bit 6 T OCR Inst. Fault bit 5 S OCR Inst. Fault bit 4 R OCR Inst. Fault bit 3 Reserved bit 2 T OCR Fault bit 1 S OCR Fault bit 0 R OCR Fault 0=Not Fault, 1=Fault	2	R	A		unsigned 16

3. LINE CHECK (Input Register)

Code	Address	Parameter	Bytes	R/W	Unit	Data Mag.	Data Type
Analog Data Check (Current Monitoring)							
04h	30010	R PHASE Current ex) Current Data HI Data LO 0.5A 00h 05h ~ 100A 03h E8h	2	R	A	1000	unsigned 16
		30011					
	30012	T PHASE Current					

4. SET CHECK (Holding Register)

4 - 1 Frequency Set

Code	Address	Parameter	Step	Data Mag.	Bytes	R/W	Unit	Data Type
Channel Operating Set (Line Frequency Set)								
03h	40000	System Frequency Setting Data HI Data LO 60Hz 00h 00h 50Hz 00h 01h			2	R/W	Hz	unsigned 16

4 - 2 OCR Set

Code	Address	Parameter	Step	Data Mag.	Bytes	R/W	Unit	Data Type
Channel Operating Set (OCR Set)								
03h	40001	Time Delay OCR Current Setting TAP Range (0.5 ~ 12.5 A) ex) Current Data HI Data LO Lock 00h 00h 0.5A ~ 00h 05h 12.5A 00h 7Dh	1		2	R/W	A	unsigned 16
		Instantaneous OCR Current Setting TAP Range (5 ~ 80 A) ex) Current Data HI Data LO Lock 00h 00h 5A ~ 00h 05h 80A 00h 50h						
	OCR Time Lever Setting TAP Range (0.5 ~ 10) ex) Time Data HI Data LO 0.5 ~ 00h 05h 10 00h 64h							
	OCR Time Curve Setting Data HI Data LO Definite Inverse 00h 00h Normal Inverse 00h 01h Very Inverse 00h 02h Extremely Inverse 00h 03h							
	Instantaneous OCR Time Delay Setting TAP Range (0 ~ 20) ×10msec ex) Time Data HI Data LO 0 ~ 00h 00h 20 00h 15h							

5. FAULT CHECK (Holding Register)

Code	Address	Parameter	Step	Data Mag.	Bytes	R/W	Unit	Data Type	
Fault Data Check									
03h	40006	Latest Fault Phase Data HI bit 15 ~ bit 8 Reserved Data LOW bit 7 Reserved bit 6 T Phase Inst. Fault bit 5 S Phase Inst. Fault bit 4 R Phase Inst. Fault bit 3 Reserved bit 2 T Phase TOC Fault bit 1 S Phase TOC Fault bit 0 R Phase TOC Fault 0=Not Fault, 1=Fault			2	R		unsigned 16	
	40007	Latest Fault - 1 Phase							
	40008	Latest Fault - 2 Phase							
	40009	Latest Fault - 3 Phase							
	40010	Latest Fault - 4 Phase							
	40011	Latest Fault - 5 Phase							
	40012	Latest Fault - 6 Phase							
	40013	Latest Fault - 7 Phase							
	40014	Latest Fault - 8 Phase							
	40015	Latest Fault - 9 Phase							
	40016	Latest Fault Current ex) Current Data HI Data LO 0.5A 00h 32h 10A 03h E8h 152A 3Bh 60h			10	2	R	A	unsigned 16
	40017	Latest Fault - 1 Current							
	40018	Latest Fault - 2 Current							
	40019	Latest Fault - 3 Current							
	40020	Latest Fault - 4 Current							
	40021	Latest Fault - 5 Current							
	40022	Latest Fault - 6 Current							
40023	Latest Fault - 7 Current								
40024	Latest Fault - 8 Current								
40025	Latest Fault - 9 Current								

6. FAULT CLEAR (Control Output)

Code	Address	Parameter	Bytes	R/W	Unit	Data Type
Fault Memory Clear						
05h	0	Clear Fault Memory	2	W	없음	unsigned 16