

# MODBUS MAP

(OUVR+ OVGR)

WYR-OV4DC

Rev.1

(주) 윤 영

# MODBUS System Control for OUVR

Communication Control	RS-485
Baud rate	9600, 19200, 38400 bps
Data Frame	1 Start bit, 8 Data bit, 1 Stop bit (Total 10 bit)
Parity	Non Parity
Slave No. 지정	1 ~ 32 (Device Setting)

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## 1. SYSTEM STATUS CHECK (Input Register)

Code	Address	Parameter	Bytes	R/W	Unit	Data Mag.	Data Type
System Status Check							
04h	30000	Data HI bit 15 ~ bit 8      Reserved  Data LOW bit 7 ~ bit 1      Reserved bit 0 <b>System Error</b> 0=Normal, 1=Error	2	R	A		unsigned 16

## 2. FAULT STATUS CHECK (Input Register)

Code	Address	Parameter	Bytes	R/W	Unit	Data Mag.	Data Type
Line Phase Check (Fault Monitoring)							
04h	30001	Data HI bit 15 ~ bit 8      Reserved  Data LOW bit 7 <b>G OVR Inst Fault</b> bit 6 <b>T OVR Fault</b> bit 5 <b>S OVR Fault</b> bit 4 <b>R OVR Fault</b> bit 3 <b>G OVR Fault</b> bit 2 <b>T UVR Fault</b> bit 1 <b>S UVR Fault</b> bit 0 <b>R UVR Fault</b> 0=Not Fault, 1=Fault	2	R	A		unsigned 16

### 3. LINE CHECK (Input Register)

Code	Address	Parameter	Bytes	R/W	Unit	Data Mag.	Data Type	
Analog Data Check (Line Voltage Monitoring)								
04h	30010	<b>R PHASE Volt,</b> ex) Volt.                      Data HI    Data LO 60V                        02h        58h ~ 160V                        06h        40h	2	R	A	10	unsigned 16	
		30011						<b>S PHASE Volt.</b>
		30012						<b>T PHASE Volt.</b>
		30013						<b>OVGR Volt.</b>

### 4. SET CHECK (Holding Register)

#### 4 - 1 Frequency Set

Code	Address	Parameter	Step	Data Mag.	Bytes	R/W	Unit	Data Type
Channel Operating Set (Line Frequency Set)								
03h	40000	<b>System Frequency Setting</b>  Data HI                      Data LO 60Hz                        00h        00h 50Hz                        00h        01h			2	R/W	Hz	unsigned 16

#### 4 - 2 UVR Set

Code	Address	Parameter	Step	Data Mag.	Bytes	R/W	Unit	Data Type
Channel Operating Set (UVR Set)								
03h	40001	<b>UVR Volt. Setting</b> TAP Range ( 50 ~ 110 V ) ex) Current                      Data HI    Data LO Lock                            00h       00h 45.5V                           01h       C7h 80V                                03h       20h	1		2	R/W	A	unsigned 16
	40002	<b>UVR Time Lever Setting</b> TAP Range ( 0.1 ~ 30 ) ex) Time                            Data HI    Data LO 0.5                                00h       01h ~ 30                                    01h       2Ch						
	40003	<b>UVR Time Curve Setting</b> Data HI    Data LO Definite Inverse            00h       00h Normal Inverse              00h       01h						

#### 4 - 3 OVR Set

Code	Address	Parameter	Step	Data Mag.	Bytes	R/W	Unit	Data Type
Channel Operating Set (OVR)								
03h	40004	<b>OVR Volt. Setting</b> TAP Range ( 90 ~ 150V ) ex) Volt.                            Data HI    Data LO Lock                            00h       00h 45.5V                           01h       C7h 142V                              05h       8Ch	1		2	R/W	A	unsigned 16
	40005	<b>OVR Time Lever Setting</b> TAP Range ( 0.1 ~ 30 ) ex) Time                            Data HI    Data LO 0.5                                00h       01h ~ 30                                    01h       2Ch						
	40006	<b>OVR Time Curve Setting</b> Data HI    Data LO Definite Inverse            00h       00h Normal Inverse              00h       01h						

#### 4 - 4 OVGR Set

Code	Address	Parameter	Step	Data Mag.	Bytes	R/W	Unit	Data Type
Channel Operating Set (Current Set)								
03h	40007	<b>OVGR Time Delay Volt. Setting</b> TAP Range ( 10 ~ 100V) ex) Volt.                      Data HI    Data LO Lock                        00h        00h 45.5V                        01h        C7h 100V                         03h        E8h	1		2	R/W	A	unsigned 16
	40008	<b>OVGR Instantaneous Volt. Setting</b> TAP Range ( 100 ~ 190V) ex) Volt.                      Data HI    Data LO Lock                        00h        00h 100V                        03h        E8h 185V                        07h        3Ah						
	40009	<b>OVGR Time Lever Setting</b> TAP Range ( 0.1 ~ 30 ) ex) Time                        Data HI    Data LO 0.5                            00h        01h ~ 30                                01h        2Ch						
	40010	<b>OVGR Time Curve Setting</b>  Definite Inverse        Data HI    Data LO 00h        00h Normal Inverse         00h        01h						

## 5. FAULT CHECK (Holding Register)

Code	Address	Parameter	Step	Data Mag.	Bytes	R/W	Unit	Data Type		
Fault Data Check										
03h	40011	<b>Latest Fault Line</b>								
		Data HI bit 15 ~ bit 8      Reserved								
		Data LOW								
		bit 7 <b>G OVR Inst Fault</b>								
		bit 6 <b>T OVR Fault</b>								
		bit 5 <b>S OVR Fault</b>								
		bit 4 <b>R OVR Fault</b>								
		bit 3 <b>G OVR Fault</b>								
		bit 2 <b>T UVR Fault</b>				2	R		unsigned 16	
		bit 1 <b>S UVR Fault</b>								
		bit 0 <b>R UVR Fault</b>								
				0=Not Fault, 1=Fault						
			40012	Latest Fault - 1 Line						
	40013	Latest Fault - 2 Line								
	40014	Latest Fault - 3 Line								
	40015	Latest Fault - 4 Line								
	40016	Latest Fault - 5 Line								
	40017	Latest Fault - 6 Line								
	40018	Latest Fault - 7 Line								
	40019	Latest Fault - 8 Line								
	40020	Latest Fault - 9 Line								
		<b>Latest Fault Volt.</b>								
	40021	ex) Volt.            Data HI Data LO 82.5V            03h      39h 152V            05h      F0h								
	40022	Latest Fault - 1 Volt.								
	40023	Latest Fault - 2 Volt.		10	2	R	A	unsigned 16		
	40024	Latest Fault - 3 Volt.								
	40025	Latest Fault - 4 Volt.								
	40026	Latest Fault - 5 Volt.								
	40027	Latest Fault - 6 Volt.								
	40028	Latest Fault - 7 Volt.								
	40029	Latest Fault - 8 Volt.								
	40030	Latest Fault - 9 Volt.								

## 6. FAULT CLEAR (Control Output)

Code	Address	Parameter	Bytes	R/W	Unit	Data Type
Fault Memory Clear						
05h	0	Clear Fault Memory	2	W	없음	unsigned 16